

Figure 1: Composite Device

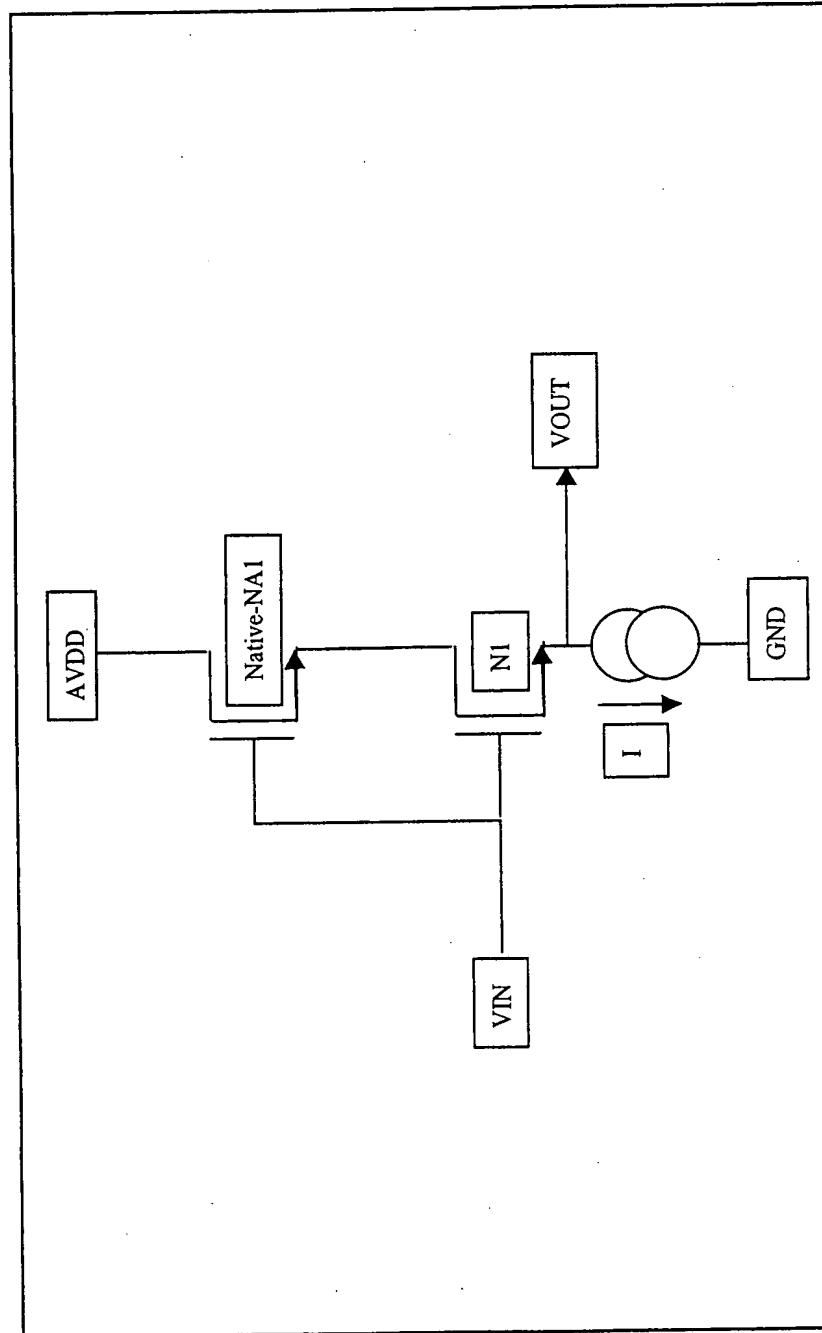
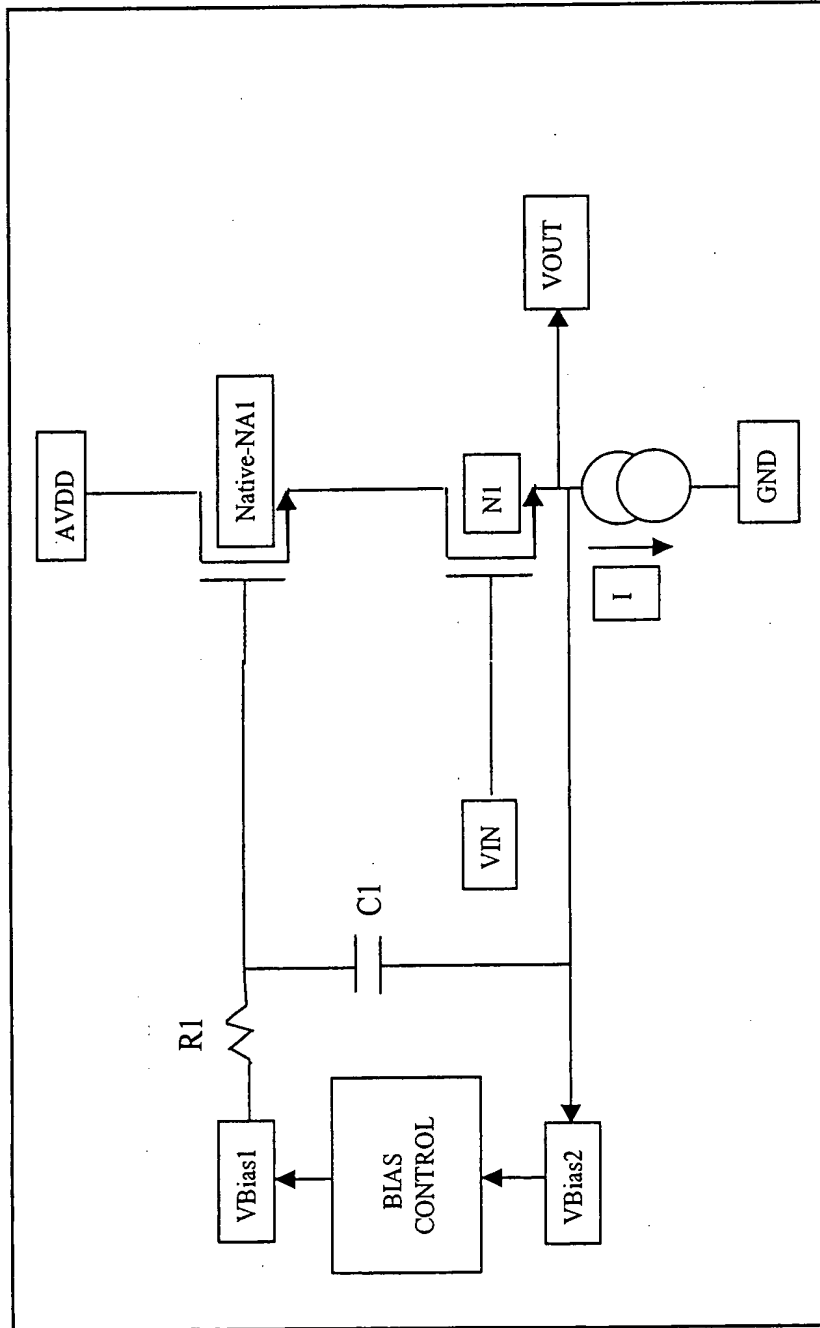


Figure 2: Source-Follower Configuration



**Figure 3: AC-Coupled Bias of Native Gate**

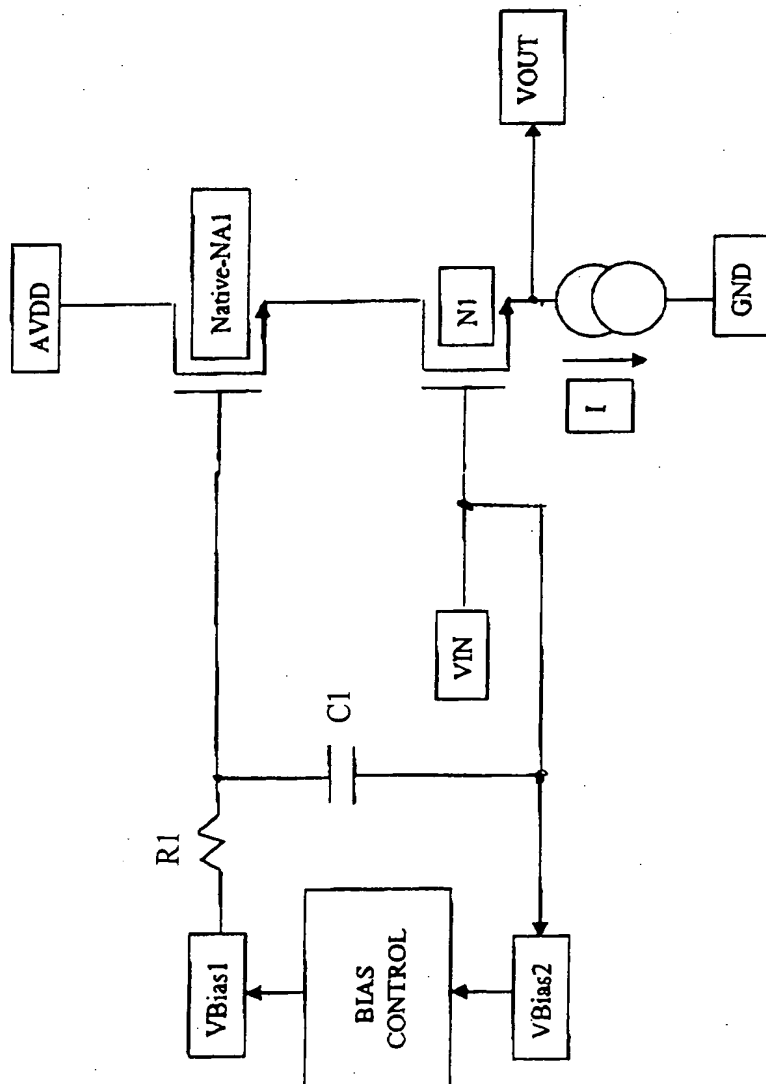


Figure 4

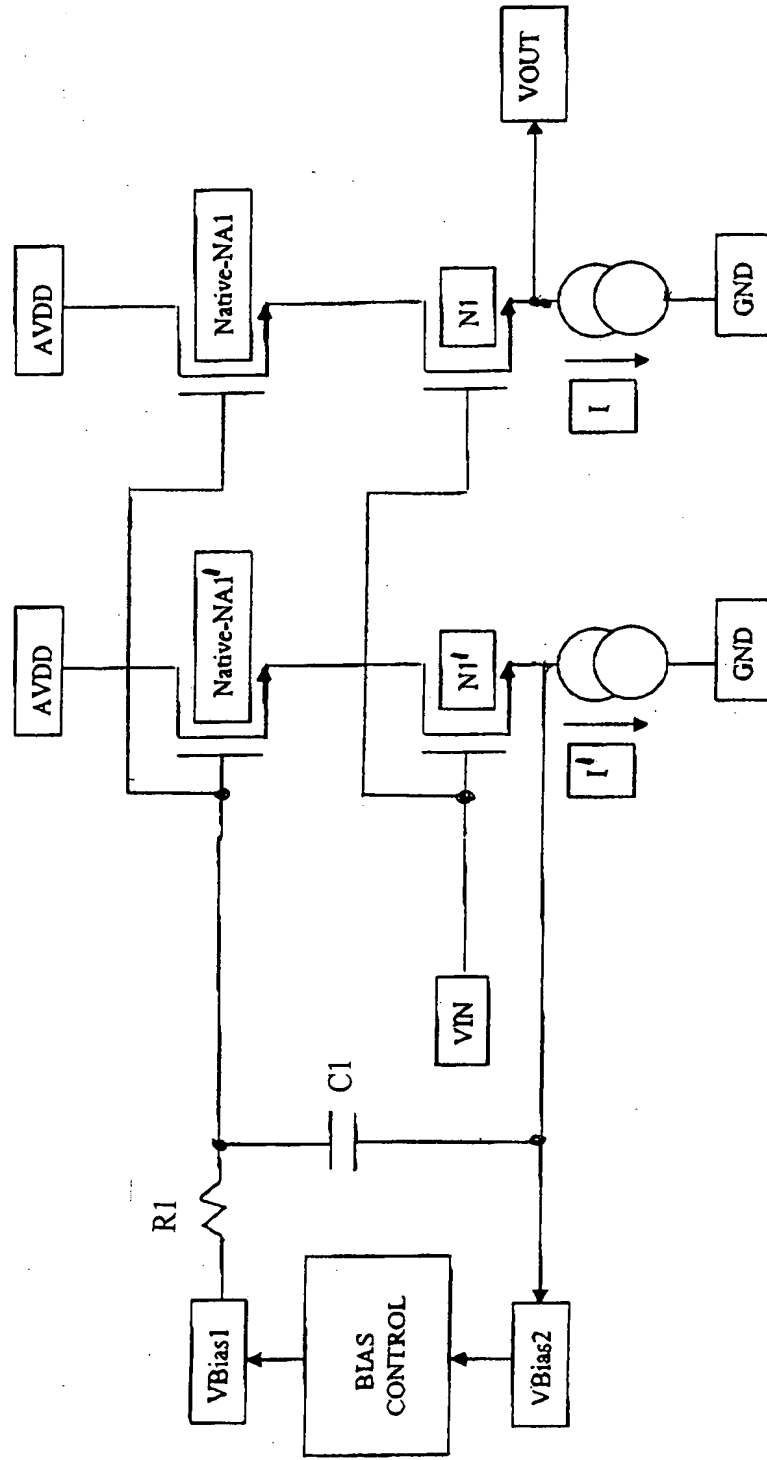


Figure 5